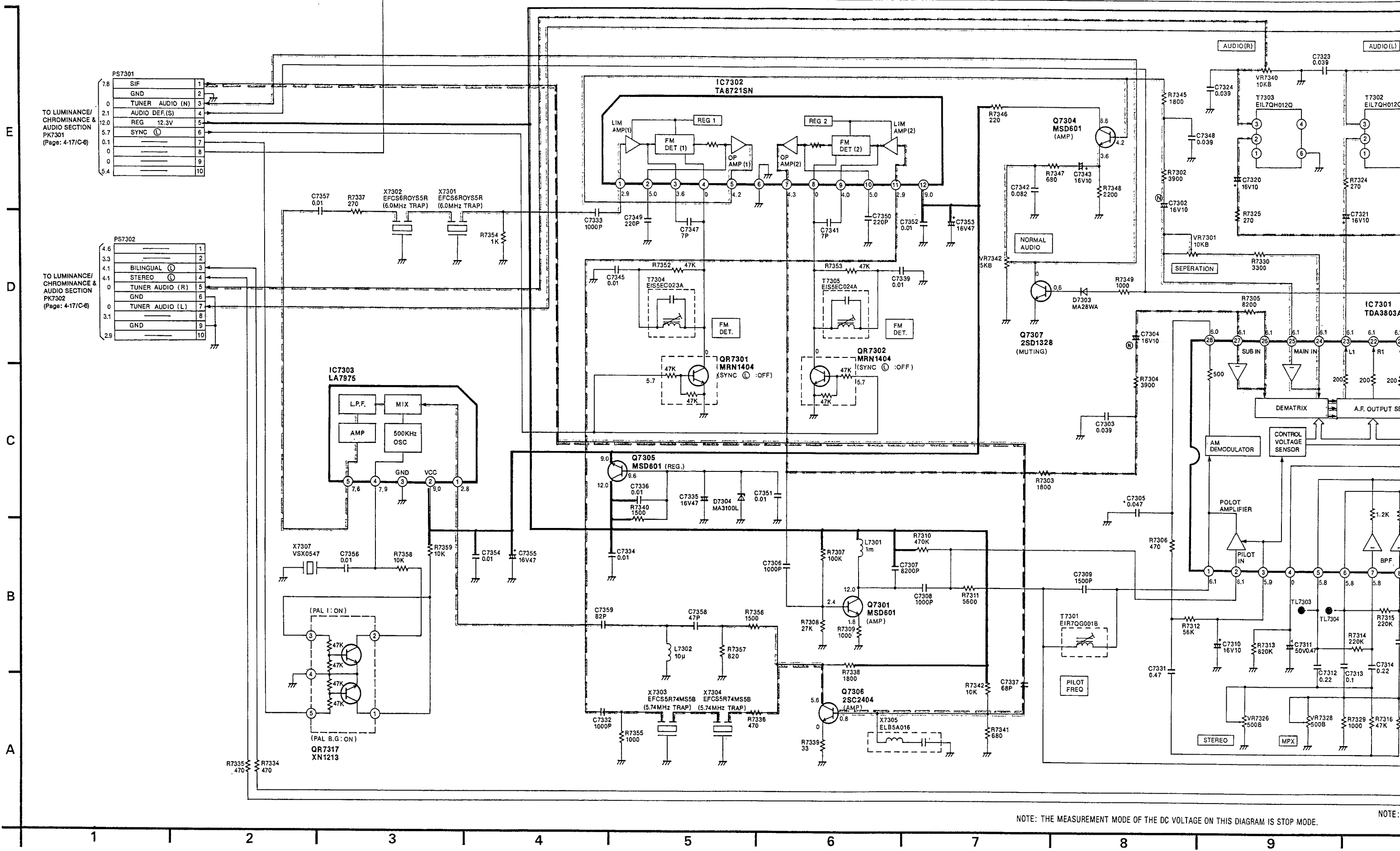


# 4-21. DECODER PACK SCHEMATIC DIAGRAM (NV-HD90EE)

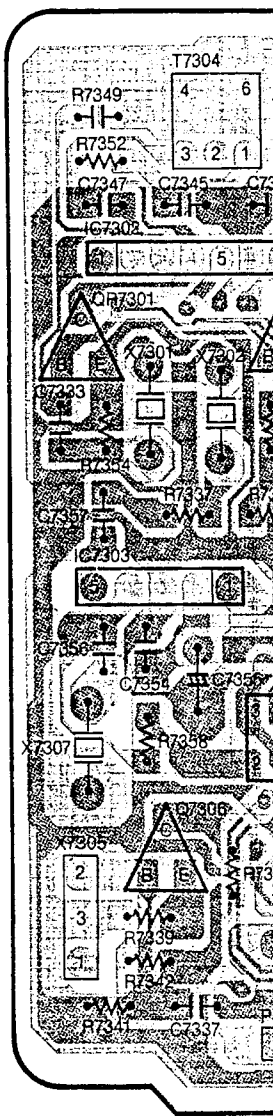
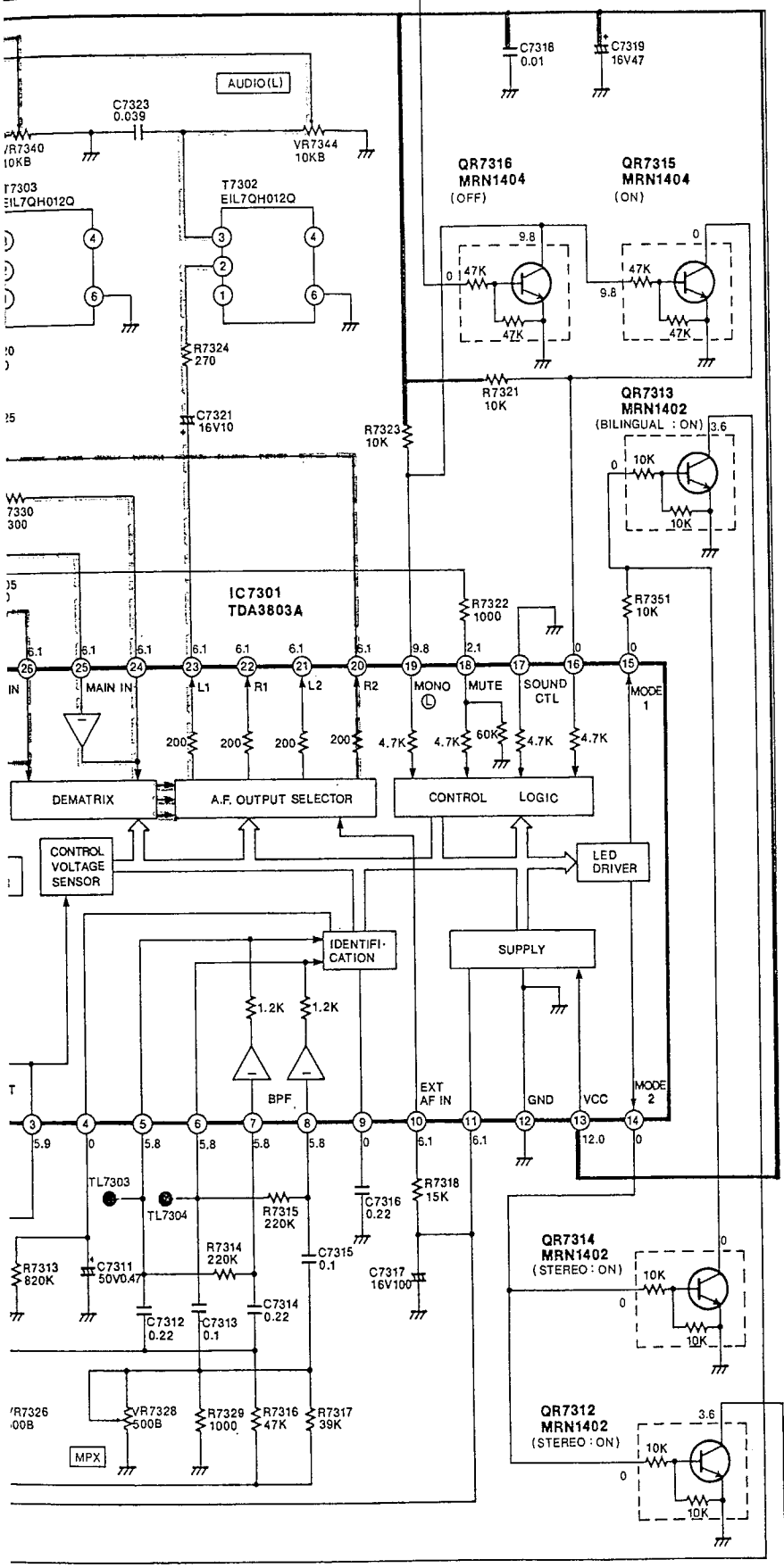
## AUDIO MAIN SIGNAL PATH



NOTE: THE MEASUREMENT MODE OF THE DC VOLTAGE ON THIS DIAGRAM IS STOP MODE.

NAL PATH

AUDIO SUB SIGNAL PATH



NOTE: DO NOT USE ANY PART NUMBER SHOWN ON THIS SCHEMATIC DIAGRAM FOR ORDERING. WHEN YOU ORDER A PART, PLEASE REFER TO PARTS LIST.

RAM IS STOP MODE.

9 | 10 | 11 | 1